XJTAG schedules another series of its ‘highly-rated’ free boundary scan workshops

Submitted by: Martin Brooke Associates
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CAMBRIDGE, England, February 11, 2009 - XJTAG, a leading global supplier of IEEE Std. 1149.x boundary scan development systems, has scheduled another series of its ‘highly-rated’ free ‘Introduction to boundary scan’ training workshops during February, March, April and May.

The full-day sessions, which will run throughout the spring and summer at its Cambridge, England headquarters, are designed to provide design, development, test, and production engineers with a practical hands-on introduction to boundary scan.

“The workshops are proving very popular with engineers who like the fact that they gain some practical experience of using a JTAG system coupled with learning some of the theory behind what the tools are doing,” said Stuart White, XJTAG’s workshop co-ordinator. “We explain how boundary scan can be used to improve designs, reduce respins and enhance test coverage, fault diagnosis and production yields on complex BGA-populated circuits.”

The free workshops will give an overview of the IEEE 1149.x standard, demonstrate how to communicate with the JTAG chain and interact with JTAG devices, such as FPGAs. They will provide an introduction to board testing using the JTAG chain, explain how to describe a circuit to enable JTAG testing and how to run an infrastructure connection test. Finally, the trainers will outline how to test non-JTAG elements of a board design using boundary scan.

The XJTAG development system is a cost-effective solution for debugging, testing and programming electronic printed circuit boards and systems throughout the product lifecycle. XJTAG reduces the time and cost of board development by allowing early development of reconfigurable test scripts that can be used from design validation through prototype debugging and on into manufacturing.

XJTAG is used across a variety of sectors by market-leading companies (designers, developers, OEMs and contract manufacturers) including, among others, Aeroflex, ARM, Broadcom, CRFS, Curtiss-Wright, DiagnoSYS, Link Research, Thales UK, TVonics, Wavecom and Westinghouse Rail Systems.

To book a place on one of the XJTAG workshops (one person per company only), register at www.xjtag.com/workshop.php.

For more information about the workshops, please contact XJTAG, The Irwin Centre, Scotland Road, Dry Drayton, Cambridge CB23 8AR, UK. Telephone +44 (0) 1954 213888, fax +44 (0) 1954 211565 or email enquiries@xjtag.com. Alternatively, visit www.xjtag.com.

Photography to accompany this news release is available at:
http://www.xjtag.com/company/press/

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About XJTAG (www.xjtag.com)

XJTAG is a leading global supplier of IEEE Std. 1149.x compliant boundary scan development systems. XJTAG offers a highly competitive solution for designers and developers of electronic printed circuit boards and systems. Utilising XJTAG allows the circuit design, development and manufacturing process to be shortened significantly by facilitating early development of reconfigurable and re-useable test scripts that can be used from design validation through prototype debugging and on into manufacturing.

The XJTAG development system enables engineers to debug and test a high proportion of the circuit (both JTAG and non-JTAG/cluster devices) including BGA and chip scale packages, such as SDRAMs, Ethernet controllers, video interfaces, Flash memories, FPGAs and microprocessors. XJTAG also enables In-System Programming of FPGAs, CPLDs and Flash memories, and XJTAG clients have access to a library of device-centric test scripts.

XJTAG is based in Cambridge, UK, and is part of the Cambridge Technology Group. XJTAG has a global network of distributors servicing Europe, the Far East, North and South America, the Middle East and Australasia.

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